

Firmware Build_FW1046_FF1471 (30.10.2015)
DspFirmware: Build 1046
Release Date: Date: 2015-10-22 14:16:47 +0200 (Do, 22 Okt 2015)
SVN Revision: Revision: 21106
RLID: 4&5&6 & 16
Remarks: supporting TSx5x RevB,RevC&RevD and TSP350&700 Rev0,Rev1 Drives,

Changes since last Build:

- version number DSP_FWID increased to 1046

SPECIAL_VERSION (not released)

- 1046_1 implementation for special summation inputs via TamVars for iD with TamVar2, iQ with TamVar3 and position X with TamVar4. Do not use TamVars(2..4) in Tama prog if FW 1045_1 is installed!
- 1046_2 implementation for compensation of nonlinear PWM output using TamVar0,1,2,24 and TamIntVar0-4. Do not use TamVar0,1,2,24 and TamIntVar0-4 in Tama prog if FW 1045_2 is installed!

FpgaFirmware: Build 1471
Release Date: 2015-10-27

Changes since last Release:

- 1) new hardware type TOAII HR1
- 2) new hardware type TOEA1 HR1
- 3) new hardware type TOEA2 HR1 Analog
- 4) new hardware type TOEA2 HR1 FFT
- 5) TSD80 with 20MHz current sampling and new filter
- 6) sending latched STO signals to DSP
- 7) TSD80 with dual encoder support

Refactorings:

- Xilinx build process with XST and PlanAhead support
 - removal of configurations
 - different architectures changed to different components
 - avoiding assignments of vectors with different low and high indices
 - avoiding record field attributes e.g. data.x.length
 - removing assert in synthesizable code
 - avoiding identical component names from different libraries
 - avoiding equal names after record field substitution e.g. data.x will be the same as data_x
 - avoiding loops with bidirectional signals (e.g. mdio)
- Cdc and Sync library
- Firmware Id in a separate top level file
- Option Module port data types
- Monitor_IO Serializer with simpler reset logic (timing problems)
- Option Module in general
- Original and modified PciE code with same interface
- Eim Interface, adding debug utilities
- Fifo Interface
- Creating extended local bus library
- Removing TSD80 local bus interface

Bug Fixes

- Analog encoder logging swapped x and y
- TSD80 and MCI42 leds
- PWM local bus device did not reflect the state of the STO signals
- PLL locked problems