Firmware Build_FW1045_FF1395 (29.06.2015)

DspFirmware: **Build 1045**
Release Date: **Date: 2015-06-26 17:16:32 +0200 (Fr, 26 Jun 2015)**
SVN Revision: **Revision: 20306**
RLID: **4&5&6 & 16**
Remarks: **supporting TSx5x RevB,RevC&RevD and TSP350&700 RevA,RevB Drives,**

Changes since last Build:
- version number DSP_FWID increased to 1045
- NEW: implemented more robust path interpolation with capability to correct one or two missing samples
- FIX: highspeed timestamp entry in iso messages had 100us lag. corrected now
- FIX: prevent from CurrentLimitMotorOrDrive error condition when switching to 2DC

- version number DSP_FWID increased to 1044
- FIX: STO inconsistent event in NotReady state showed short intermediate Ready before showing the Incon. Fault
- CHG: protect flash anyway after programming
- NEW: support for TL100 HR03 (inactive DSP code after start up and SPI release)
- NEW: nonLinearity compensation test mode for 3 phase motors (_2 version)

SPECIAL_VERSION (not released)
- 1045_1 implementation for special summation inputs via TamVars for iD with TamVar2, iQ with TamVar3 and position X with TamVar4. Do not use TamVars(2..4) in Tama prog if FW 1045_1 is installed!
- 1045_2 implementation for compensation of nonlinear PWM output using TamVar0,1,2,24 and TamIntVar0-4. Do not use TamVar0,1,2,24 and TamIntVar0-4 in Tama prog if FW 1045_2 is installed!

FpgaFirmware: **Build 1395**
Release Date: **2015-06-29**

Changes since last Release:

1) New hardware type TLOC100 HR3
2) New hardware type TLC100 HR3
3) New hardware type TL100 HR3
4) New hardware type MCI42 HR0
5) Analog encoder amplitude error bugfix
6) Monitors with abo send
7) Pwm spread spectrum and soft switch on removed
8) Option module support
9) Complete pll refactoring with XCO-support

10) TSD80 Monitors with bidirectional abo's
11) extio with pull up instead of pull down

Refactorings:
- Clock domain crossing
- Data types
- Counters
- Memories
- Fifos
- Local Bus
- Current converter
- Phases a+c to u+v+w
- Monitors
- 3-wire and 4-wire spi