

**Firmware Build\_FW1052\_FF1544 (04.07.2018)****DspFirmware: Build 1052****Release Date: \$Date: 2018-07-04 15:09:22 +0200 (Mi, 04 Jul 2018) \$****SVN Revision: \$Revision: 27882 \$****RLID: 4&5&6 & 16****Remarks: supporting TSx5x RevB,RevC&RevD and TSP350&700 Rev0,Rev1 Drives,****New features:**

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**Bug Fixes:**

- PathPlanner did not work as expected for the case when a running stop command is superseded by move command (modulo recalculation worked with the new move command instead of the stop command)

**Changes:**

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**FpgaFirmware: Build 1544****Release Date: 2018-04-09****New features:**

- Sigma-Delta 2nd order replaced by 1st order  
- Trialink Master Oscillator

**Refactoring:**

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**Bug Fixes:**

- PLL Locked threshold level changed from -64...63 to -512...511 due to problems with thermal caused frequency jumps. these jumps led to a trialink unlocked state throwing an error. The new threshold value is going back to the threshold of FW1302.