

<b>Firmware Build_FW1053_FF1544 (05.09.2018)</b>
<p><b>DspFirmware: Build 1053</b>  <b>Release Date: \$Date: 2018-09-05 15:11:35 +0200 (Mi, 05 Sep 2018) \$</b>  <b>SVN Revision: \$Revision: 28316 \$</b>  <b>RLID: 4&amp;5&amp;6 &amp; 16</b>  <b>Remarks: supporting TSx5x RevB,RevC&amp;RevD and TSP350&amp;700 Rev0,Rev1 Drives,</b></p> <p><b>New features:</b>                      -</p> <p><b>Bug Fixes:</b>                      -</p> <p><b>Changes:</b>                      -</p>
<p><b>FpgaFirmware: Build 1544</b>  <b>Release Date: 2018-04-09</b></p> <p><b>New features:</b>                      - Sigma-Delta 2nd order replaced by 1st order                      - Trialink Master Oscillator</p> <p><b>Refactoring:</b>                      -</p> <p><b>Bug Fixes:</b>                      - PLL Locked threshold level changed from -64...63 to -512...511 due to problems with thermal caused frequency jumps. these jumps led to a trialink unlocked state throwing an error. The new threshold value is a going back to the threshold of FW1302.</p>
<p><b>MonLogic:</b></p> <p><b>Bug Fixes:</b>                      - fixed communication timeout during STO fault simulations</p>